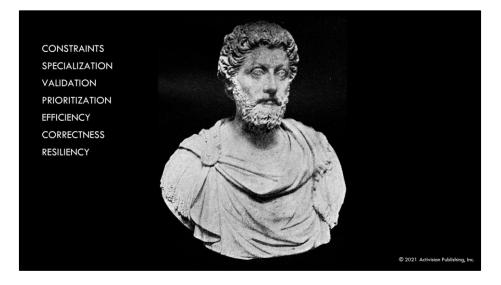


Good morning everyone, my name's Michael Vance.

Thanks for joining us today to talk about something we've been very excited to get going for a while, which is this series on rendering engine architecture.

While I am now the CTO of Activision Publishing, for most of my twenty years in this industry I have been a systems programmer.

Primarily working on rendering code or rendering-adjacent code.



The idea of this series of talks was to discuss the varying approaches to rendering engine architecture in games. For instance what different decisions do we make if we're a bespoke engine, vs a platform, vs a licensed engine technology?

In our case as a bespoke piece of technology for shipping a specific game, we have some philosophical ideas that are important to us.

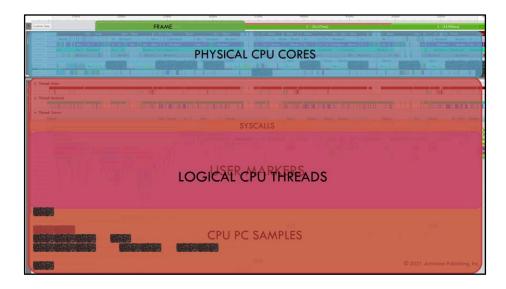
Constraints, how to choose them and the specialization they unlock, and the importance of validating them over time. Prioritization of work based on knowing the technology's use end to end.

How specialization unlocks efficiency.

How we can reason about correctness with large knowledge about the technology.

And how we can engineer for resiliency in our code and why that's valuable.

[MARCUS AURELIUS]



My daughters like to dispel notions of the excitement of working on games by describing it as "staring at a screen for several hours with an annoyed look".

Which is pretty much spot on.

What I was usually looking at was a performance analysis tool.

And this talk is largely "a tale told in analysis tool captures, with some other pretty pictures to boot."

At this event there are possibly some people who haven't worked in these tools that much.

So I'm going to do a quick overview of what we'll be looking at, since this tool forms the basis of a lot of my slides.

This is Razor CPU, which is our preferred analysis tool for CPU work and understanding of execution.

CLICK

At the very top we have a timeline with a frame marker. Time moves from left to right.

CLICK

Then below that the physical CPU layout showing our named threads.

You'll notice on this capture that we have two clusters with four cores each: each cluster has its own L1 but they share an L2.

CLICK

This next section is the logical thread view.

CLICK, CLICK

And these are syscalls, you can see the gaps on the logical thread markers correspond to operating system calls like synchronization primitives such as mutexes and condition variables.

CLICK

User markers which are pushed and popped by the client with custom names are shown here.

CLICK

And down here you have PC samples from the executable including callstack walks.

Custom Sync			1 - (15.979ms)	1				2 - (1
Chaster 0 - CPU 2 Work P Dot: Data Workert Chaster 0 - CPU 3 Rackerst	Main Main V Worker0 Worker1 C C V Worker1 Ba Backern1	Work: V W: Worker Worker0	in Main W/ Wi Worker0 W Wi Worker1 Backend Backend	3 Main	WorkerD Worker1 Backend	Worker0 Work Data I Worker1	Backe	
Cluster 1 - CPU 4 Wor Wor Wor W Cluster 1 - CPU 5 Cluster 1 - CPU 6 sund m Sound m Soc	Worker2	Work: Worker?	Work: Worker2 Server Server	Server Server Sound mix three		Wer Werk	Worker2 Server	ound mix Soc Sound
▶ Thread: Main								
> Thread: Backend								i i i i eter
and and a								and and and and
Thread: Server					a iii	Internal III		_
P Inread: wonteru	1						ui) .	Lillinge
Thread: Worker1					_			
> Thread: Worker2								_
Thread: Database								erf.on
umbi op								Lont
Ihread: Sound mix thread orf.wat								
Thread: Sound Umbra Thread								_
> Thread: Sound decode thread		INLANCE IN CONTRACTOR OF CONTO						
Inct Inct Inct Inct Inct		locti levi locti	evf_wait		loct			f_wait iocti
evf.wat			evf_wait			evi, wait		
Thread: Thread:	nanosleep	nanosleep		nanosleep		nanosleep		nanosleep
> Thread:		, Contraction						
Thread: Cinematic							© 2021 Activ	ision Publishing,

I like to look at things from a ground truth state on the hardware first, so let's begin there, and we'll take a look at it from a higher logical pipeline view later.

CLICK

Our titles operate around three primary threads which unfortunately have a few names each: main/client/frontend for the first, render/backend, and server.

We have multiple names because some have multiple systems responsibility of each, since that can be a little confusing we'll try to use them consistently as frontend/backend/server because this talk is about rendering and frontend/backend is one of the core distinctions.

CLICK

We also instantiate worker command threads to cover remaining cores. Database is a special file service thread that cooperatively shares a core with a worker.

CLICK

For those unfamiliar, on some consoles the last core is shared partially with the operating system and so you want to schedule work that is robust to latency on there, and audio can mix ahead in time, so we have a large group of audio

threads that are setup for that purpose. CLICK

Last, there are lightweight task threads that exist with looser affinity to cover low frequency operations such as save game processing, cinematics, etc.

\$Bmt	20ms	22ms	24ms	25ms	28ms	30ms	32ms
Cautom Syne:			1-	(15.979ms)			
Challer D - CPU II	Main	Main	Main Main Main	1 Ma			Main
	WorkerD WorkerO	Work W Wo				Workee0	Worker0
Chater 9 - CPU 2 Wo Del Del Ontri Chater 9 - CPU 3	Worker1 Worker1	Worker1 1 Worker1	Workert W W:			Nori Data Workert	
	Whe Wo: Worker?	Backend Work: Worker	2 Work: Worker2	a	Backend	Wo Wool	Backend
Chater 1 - CPU 5	AND MODIFIED	THUS WORK	Conversion Service	Server Server	Server Server	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Server
Clinter 1 - CPU 6 sund m. Sound n. Sou	STATISTICS III	Sour Sour So. 5	South and a second state		and mix they	1000000	Sound mis
Chatter 5-CPU /							
and the second se	1						
			-				-
Me Main	Main	Main	Mai Main Main Main	E Ma	in 🔰	Main	Min
R. BendlerScotter		D		add packet ents	a bie bi		CC R. RenderScene
wait umbra dyn ent camena	wait add shadows draw all ci-	draw 20 wit honlend cm	Orbi CG. Processina:	P will end exec ent wait entities	Di CG.P	0	SP Walt wait unbra dyn er
R_ProcessWorkerCmdsWithTimeout umbia static camera	R_ProcessWorkerC/ DrawD/	CG_Draw2D ProcessWorker		weit our wort only up	date entitie:		R.P. R.ProcessWorker
	United by Hawking	LLA R Generatel		Bald C		100	Umi Bapi
Marker Level 6	Ma Link Pr	R Drawi	7				10
	Tank 1	A CALL BUD			1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1	1 11	
	H				100	1	1
	L.	- III					T
And in Local dis						- 11	
States States	or his sector is a sector his or						
Contraction of the second second second			(C
CONTRACTOR OF THE OWNER.	and a second second						
and the state of t	in the cash	est, tout).					
method							
						1	
er husberlifersite	start (man, gast - second) (200	sau.ga/s//gant_bactor/s/gitpant_auc					
> Thread: Backend							
	and the second se	1.1.1.1.1.	CONTRACTOR OF A DESCRIPTION OF A DESCRIP			And in case of the local division of the	CONTRACTOR DESCRIPTION
					1	비해도 정비 이 또 않	
> Thread: Server							
> Thread: Worker0							
> Thread: Worker1							

Let's look at the frontend thread first.

It's responsible for network packet gathering, input sampling, and scene graph traversal, which is our frontend rendering work.

Most of these operations will spawn worker commands that operate in a fork/join model in order to utilize all available CPU bandwidth.

CLICK

So here I'm highlighting first the "early" part of our main thread, which involves pumping the network on the client side and processing the snapshots as they come in from a raw dispatch perspective.

And then we have the actual work interpretation of the snapshot updates, which you can see here are marked by many waits on forked work.

Those waits then turn into assists to perform actual work to ensure we're making forward progress.

CLICK

Then our player handling is processed, we do this as late as possible so that input sampling is more closely aligned with kicking graphics work.

CLICK

Last, the frontend scene rendering which is the scene graph traversal phase for the world state.

nton Synt			1 - (15.979m)				
uster 0 - CPU 0 uster 0 - CPU 1 Worker		Mail Mail M Work W. Worke Worker0	ain Main Main Worker0	3. Main	Worker0 Works	Mein Mein Worker	
uster 0 - CPU 2 W/s On Data Works	ert Workert E	rker1 1 Worker1 Warker1	Wo Worker1			Data Worker1	
uster 1 - OPU 4 Www	W= Woder/2	Backend Work: Worker>	Backend Backend Worke Worker2		Backend	Wo Was Worked	Backend
uster 1 - CPU F sund m - Sound m - Sou			Supercontractor Serventeere	Sound mix the	Se Server	Ser. Server	Sound mit So
user 1- CPU 7		Sour Soury So So Sou		Sound mix the			Sound mis So
Thread: Main							
Thread: Barrind						H	
ackend Backer	nd Be Backens	Backerd	Backent Backand		Backend		Reckered
		ExecutedonderOnde					De
Exerce RendererSleep		ence ends shadow	leiec and op	igur		RE exercited even	post3d an en
R ProcessWorkerCrickWithTeneour add wait wait add st		Sun Sun Shadow	Mas Pr		Opaquett Opaquetter5 R.Draw5 R.DrawSurfs		
SMode	Physics Tank Pr					1	11.000
larber Love 10	Lock Harril						
later Law 8	Task						
laites Lean II Veikis Laine III	Task Marc 2						
helper Lower 11 helper Lower 1	10	L.					
later treat to							
NSR, Understand, pr-link (HES-underschaption) and		Notethar/Nacio-Stapfort.ofDate or side (1011) effort					
II, DrawTiteret/Person(Ob-Gendtia/Greater, Materna		Not, Boat, Nort, Nort, Nort, and group out on 2014 York Data Ta					
HEALIN, ORCANNON/HITTEEP, HIM FURCHES	#3Plags CochedCityst*)						
AdditingD periodic million (Drivershite)							
SietusParaFyeCher pArgsZetternall()/hC+sklbalCores							
ProDow(D)(Credit/Han*)							
No. of Concession, Name							

The second major thread, the backend thread.

It is historically responsible for command buffer generation.

But we'll see later that it is more of a simple "graphics device ownership" thread given that so much of the actual command buffer generation happens in parallel on other cores.

And is kicked as early as possible from the frontend.

CLICK

Here I've highlighted what is our command buffer submission on the backend.

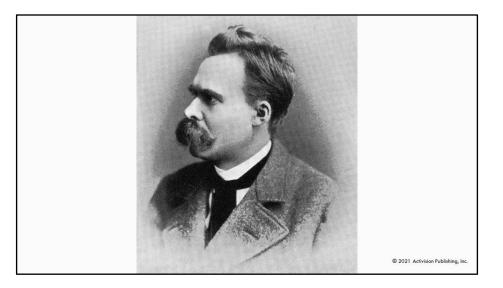
You can see a few chunks blow up, this is where the backend itself has picked up jobs it needs immediately and started work on them.

The very heavy hashing in between those is it just submitting command buffer that has already been finished by completed jobs.

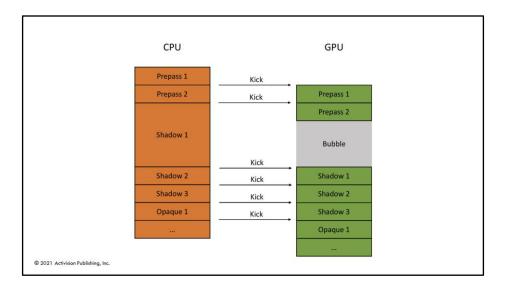
CLICK

You can also see earlier in the frame where the backend thread is idle that it is picking up some random jobs like processing frontend command and physics.

We're sensitive to the idea that it might pick up a long job which causes bubbling so we guard against that with custom job dependency rules.



Speaking of jobs going long and latency, let's have a brief philosophical interlude. [NIETZSCHE]



We think in terms of the GPU always being our critical path.

That is, the GPU should never be idle, it should be presenting images as fast as possible to the user, and never starved for work.

If the GPU is idle, we feel we have made a mistake in our work scheduling.

A bubble is the term we have for that blank space that shows up and no work is being done, pushing the frame farther out.

When the frame is pushed farther out it increase the odds we will miss our vsync/flip scheduling.

And that will cause a frame to persist, cause latency to spike, and create that feeling of "judder" you get with occasional repeated frames.

Thus we are very careful to avoid bubbling on the GPU, scheduling jobs at enough of a granularity to ensure they are able to feed the GPU command buffer at a sustained rate with no gaps.

And we are willing to trade non-trivial amounts of CPU to improve GPU performance in many cases.

You can think of trying to move such bubbles back through time.

From the GPU to the backend thread which submits work to the frontend thread which creates the input to the command buffer generation, all the way back to the best place to be idle.

Which is right before input sampling because all your work is done ahead of time and you want to schedule input

sampling as late in the pipeline as possible to reduce input to display latency.

15ms	20m3 25m3 30m3	35MS 40MS 45MS	Soms SSms
Custom Sync Custom Sync Custom Cont Dis Mai Mai	1 - (15.979ms) Mair Main D. M. Main Main Mai	2 - (16275ms) in Main Main Main Main Main Main	3 - (14.998ms)
Cluster 0 - CPU 1 Worke: 1 Cluster 0 - CPU 2 Work Work Wc	W. Worker0 V Work Worker0 Wor Worker0 V W. Worker0 Worker0 Worker1 Wor V Worker0 Worker1		Worker0 Worker0 Worker0 Worker0 Worker0 Worker1 Worker1 <t< th=""></t<>
Cluster 0 - CPU 2 - nd Back Backens Cluster 1 - CPU 4 Worker?	Baci Ba Backend Ba Backend Bac	Backerr Bac Backerr Backerr Backerr Backerr Vorker2 W V Worker2 Worker2 Worker2 Worker2 Worker2	Backer I Backers Backe
Chaster 1 - OPUS - Chaster 1 - OPUS - Sol - Sol	S.S.S.S.S.S.S.S.S.S.S.S.S.S.S.S.S.S.S.	erver Server Serve	Server Server Server Server Server
Chuster 1 - GPU 7			
Thread: Main		-Y	•••• `
> Thread: Backend			
The d: Server			
Server	Serv Server Sc 5 Server S	erver Server Server Server	Server Server Server Server
Marker po run frame Marker S SV_Frame Marker vet 2 G BunFrame	server lineout R. ProcessWorkerCrudsWithTimes		wait send mic point R.ProcessWor 5
Marker Level 3 G MainRanFrameinternal		k Umbr Que Back I	See 1
Marker wet 5 threads G, G, G, G, Marker wet 6 num threads Pro	Mainkent G RunframeForintity vertinkent Mover Actor Thick		1 1
Marker Level. 8 -			
Marker lawet: 10 Marker lawet: 11 Marker lawet: 11 Marker lawet: 11 Marker lawet: 11 Marker lawet: 12 Marker lawet: 13 Marker lawet: 12 Marker			
Marker Level 14 Marker Level 14 Marker Level 15			
Markar Level, 15 Markar Level, 15 Markar Level, 17			
		-	
and a second	(b), (d), (d), (d), (d), (d), (d), (d), (d		
R. Gettinofurti att #Carrent/Defertinofurti atCarre		00(11)	
R_GettispSurtLatLtCarrera(ObCottispSurtLatCarre	e anako unagred rec unagred ex contraposaturacia?)		© 2021 Activision Publishing, Inc
Relation Should be had believed that a court from	culture could there a World the		

Getting back to our final major thread, the server thread.

It simulates game logic decoupled from update rate of visuals, and communicates via loopback/shared memory in single player.

The server is actually run on a dedicated server instance in most situations in multi-player, although we also support running it locally on one of the clients as well.

These are the so called "listen servers".

The fact that the server runs at a separate tick rate provides us wide latitude in scheduling work on the server--we can afford expensive operations such as pathfinding in single player without affecting the client frame rate.

It also allows us to increase the server tick rate in multiplayer to ensure smooth, low latency play.

CLICK

Here I've highlighted how the server "frame" is actually around 50ms, although we're not server limited here so there is a variety of work its picking up to assist other jobs with.

CLICK CLICK CLICK

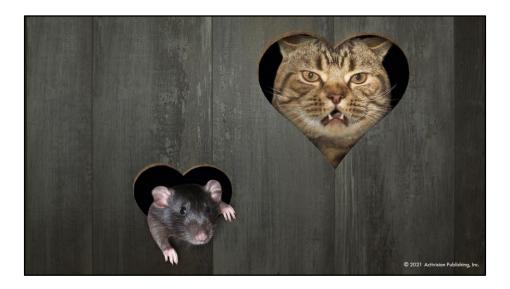
You can see how essentially three client frames fit up against the server frame.

CLICK

We have the ability to interpolate between arbitrary tick rates but as you can imagine there is a sort of "state latency" that can occur where the interpolation fidelity is under stress at lower tick rates, or when the server goes long and the client state starts to drift from the server.

Because the server's view is authoritative the client will eventually "correct" to it which can result in non-linearity/warping in the client's view.

This is really correction of the client prediction's failure to reconstruct signal due to a lack of information.



There is complexity to managing this separation between client server-both have their own views of the state of the world, and communicating and synchronizing them requires a non-trivial amount of work.

It can be tempting when programming client functionality in single-player, for instance, to want to "peek through the wall", and directly examine server state, however this creates significant problems when memory access is not properly synchronized.

This separation is an imposed constraint that allows us to leverage this decoupling, and the payoff is worth the effort, and this idea of copying for memory separation for concurrency will be a recurring one in this discussion.

Even if it does make it a pain to move an entity render flag all the way over the network so that the renderer can see the "correct" state for it at some point in time.



This is an important point and so I'll reinforce it again.

We often think of constraints as restrictive of art or design.

But carefully chosen constraints are one of the most important way to unlock large-scale transformations and optimizations in our systems.

Those constraints may have non-trivial engineering requirements but they are what will unlock the quality and performance that deliver value to content creators and players.

The most common examples of these are pre-computed lighting in static environments, but the client-server split is in a similar vein.

	North Manual No.	10 3400 June 1000	Norm Within 10944 10944	ther Marken 🔄 Industry Time 💌 🛅 Sait Made o
	Carlant from	4 - (10.543m)	\$1(1533a)	1 ten 2m Jon 4m
	Constanting Distant			
		Water? We Wei Woles? W Weiker?	Ewist Wester? 91 WE Worked ID 9	
	Chester 1 - OPU 1-1 Sector Control Control Server			hearteen hierostreen and hearteen here here here here here here here h
		and the second se		
	Main Main Main			
	Contraine		Circulture	
	ten sines state be cleare state		Deter Marine In Hardin C. Str. proceeding In prophysical and in the second secon	
		118Tome Sys ProstoWorker St. C Sys Populationari WithTreesal	Contraction of the second seco	And a second second
	PERFORMANCE IN THE REPORT OF THE	The state of the second st	I I I I I I I I I I I I I I I I I I I	
	INCOME AND AND A DO A			
	INCONSCIENTING INTERNATION CONTINUES IN THE REPORT OF THE			
	davis (prod 22			
	and a second sec	IN NAMES AND ADDRESS OF A DESCRIPTION OF A DESCRIPTIONO OF A DESCRIPTION OF A		
	R Disc If Sudmarkeed and	In the second	A Martine Contract of Contract	
	The second	[4] Cher, Proceedite And Card, William 19, Cher, Proc. 11, 6, 16, 17 Spr. Proceeditor. 40, 10 Arrival Computer Computer Systems 11, 10, 10, 244 (2011). Computer Systems 10, 10, 10, 10, 10, 10, 10, 10, 10, 10,	No. 1991 I DE CONTRACTO DE LA CONTRACTÓ DE LA	
	E Cantines Continues Continues Continues	Unites Unites Dany Statics Baption R Linterate Chaption Set Process		
	and the second se	The second		
	one cel f	AT AD	The Process	1 1 Store M Dans All and a Contract
				117mm and Links Collins Collinson
				Lifes Haby to water Delauful Dellama
				I three II Deal an Ard Deal Street
		larus Satus Satus	Server, Se	Kollen RD Des CommentFrame
	and the second s	Tauge shall server and shall server		170 fue DIMART ST_SPOTSHADDN_E_STATE
	ALTERNATION AND A STREET AND A	The Proceedination and Millimman St. The Net Proc. Well Ser Proceedination and antihilinease	A Pro- AV THAT TO BE THE WART OF MARKAN STORE SALAND TO BE AN ADDRESS OF THE PARTY	OHD Driges PABLES and
	any tool 2017 Ghadane	t hupdatener 0 1 1 0 Generalizers	I Charles I Charles I Charles I I I I I I I I I I I I I I I I I I I	St. Sign HE HE Party
© 2021 Activision Publishing	Interiment I freak I and Mar II I have	Strugdar gebre	THE I MARK I MANY	1000 Digas set of pre-
C 2021 Activision Publishing	and a local different of the second s		I MADE INVESTIGATION AND INVESTIGATION AND INCOMENTATION AND INCOM	1 125 Pho 447 107
© 2021 Activision Publishing	matter i Bill			745.5pm Hands Fallows
	Andre Land 10		11	ALL Dam In such
	Make Lond M			© 2021 Activision Publishing,

Walking back to our general threading and job model.

CLICK

Focusing on the frontend thread here.

All of these work on these "major" threads are generally executed as a series of jobs in a fork/join model with work spun out to the worker threads.

CLICK

Here you can see our main thread invoke R_RenderScene which is essentially the scene graph traversal.

Including like 1.5ms of UI code which fills me with a deep and abiding sadness.

CLICK

On some of the other cores we start executing visibility work.

CLICK

We're also executing visual effects system simulation and draw data generation.

CLICK

You can see we're actually not getting good granularity and balance of jobs here, either, we're spamming our work

queues on the main thread to see if the jobs are done, instead of helping out with any of them.

CLICK

And those of you familiar with the sight of many syscalls in Razor know that's not the greatest sign when you see that many tick marks.

10ms	20ms	22ms 1 .	24ms 26ms	28m	is 30ms	32ms	344
Caster Syst. Charter B. CPUID Charter B. CPUID Charter B. CPUID Charter B. CPUID Charter B. CPUID Charter B. CPUID	Workert D D Workert	Work: 1 W: Worker Worker0	Wi Worker0		Main WorkerD WorkerD Worker1 Work Data 1 V		
Christelle City 3 Christelle Chri	Bo Backered	Backend Work: Work: Soun Soun Sox S: Sou	Backend Backend Work: Worker2 Server Server	C Server Server		Week Worker2	nd mix Sou Sound U
> Thread: Main							_
> Thread: Backend							in ed. with
							and and
Thread: Server							
Thread: Worker0							
Thread: Worker1							LILL ext. soft
Thread: Worker2					at set 1	1 11	a later
ert.v ert.v	ant, we a				ed.wei		eef wait
Thread: Database							
Thread: Sound mix thread							
Thread: Sound Umbra Thread) U			
-							
Thread: Sound decode thread		loct of loct			loct .		wait light
Thread: AudioOut buffer thread							
Thread:	alt.				ev! wait		
nanosleep	nanosleep	nanosleep		nanosleep	nanosle	P	nanosleep
preadumtr_op							pread
Thread: Cinematic	1		Instantan	1		© 2021 Activis	ion Publishing, In

Last point from our "ground up" view.

CLICK

All of our primary threads and worker command threads have fixed processor affinity on console.

This is in order to ensure optimal memory coherency for execution due to the Jaguar SOC per-cluster cache arrangement.

We execute work on the second cluster that has the most memory orthogonality--the server thread and audio processing are specifically targeted for this.

As mentioned the server thread has its own memory representation of entities distinct from the client's, so we run the server on the second cluster.

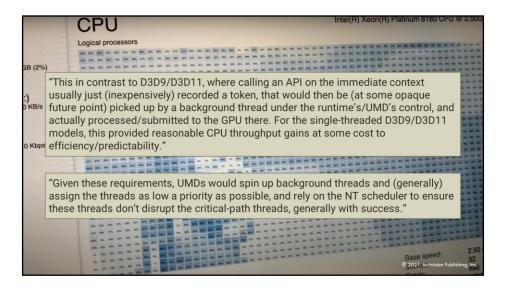
And similarly audio data has very little interaction with the main client thread outside of some shared data for systems like notetrack events.

You will notice that there is a worker thread on the second cluster, and also that we discussed previously that the server thread will pick up work when idle.

In the past we attempted various heuristics to optimally arrange which jobs got picked up by the second cluster workers.

We found that in real world scenarios it was always at least marginally advantageous to suffer the cache pollution in exchange for the extra processing throughput.

We've also worked to ensure that OS threads like network drivers, are affinitized in a way that makes the most sense for data delivery, e.g. the main thread processing snapshots coming in via dedicated servers.



We *have* attempted similar thread affinity work on PC in the past but run up against difficulty in finding a robust solution that wasn't in conflict with the OS scheduler.

CLICK

This was especially true in the D3D11 era when PC GPU drivers would perform non-trivial work, and in general we were almost always bound on CPU by the primary driver thread.

On ARM we do respect BIG.little configurations and schedule heavyweight threads as appropriate and as you would expect based on what I just explained.

CLICK

As a side note, this slide is from the D3D12 presentation on why they were restoring background worker threads as a concept in D3D12.

I'm not sure we've gone back and re-tested our thesis on processor affinity on PC recently which is definitely something we should do.

That's one of the important things about picking a set of constraints--every now and then you have to go back and reexamine them to make sure the assumptions you decided on are still valid.

CODE: Add R_SetComputeShaderLimits, implement R_SetLateAllocLimit for init blend shapes threadgroups. Resolves CE-3898 and partialy addresses CE-3706.
Coders
1) D3D12X now provides an inline method for setting shader limits that doesn't rely on deriving PSOs. This has been used to bootstrap support for graphics shader limits to support the VS late alloc code, these sets need to be deferred now till after PSO set to override them properly, so some restructuring was done to support
this. Usubjes can happen inline immediately.
2) Also add R_SetComputeShaderLimits for all platforms in order to tune shadow dispatches that have heavy L2 traffic, for instance blendshapes. This significantly
reduces blendshapes times on both platforms, although significant problems in blendshapes still exists such as overly long times on the platforms, and multiple instantiation
issues on respanse that need to be debugged. See CF-3913. 3) Testing shows that the default value for late alloc limits (22) on Durango produces results similar to the client-supplied value (31) on GBUFFER timings in
3) results shows that the denate value for rate and climits (22) on burging produces results similar to the climits day pred value (31) on Governer value (31
set; note that these calls don't roll context). Or because been we see the best times from the value the client currently supplies (63), and that is also left unchanged
but still pushed, although far less frequently as it is persistent or but still pushed, although far less frequently as it is persistent or but still pushed, although far less frequently as it is persistent or but still pushed in the state of the state
1) Henrore an old 50K 1/30 bit from the device init. (5) Timings for both platforms with async and sync or async with the blendshapes tweaks are below:
3) minings to bour placoms with async and sync or second and the bendshapes tweaks are below.
With async disabled on and the strain and the strain strai
durango
mp_seaside + torque
r_blendShapesMaxThreadGroups Time(us) 0 3220
2 1300
4 1120
8 1280
mp seaside + recon
r_blendShapesMaxThreadGroups Time(us)
0 340
2 250 4 200
* 200 © 2021 Activision Publishing, Inc

This reminds me of another digression, which is our approach to changelists.

This may sound a bit banal, but much advanced work that we do on the renderer is not just about looking forward, but also understanding why changes were made historically.

Good changelist discipline is critical for understanding this, and stands, in my mind, in stark contrast to the relative uselessness of code comments and documentation.

Most comments and documentation are a risk in my mind, because they can quickly decouple from the underlying system implementation.

Changelist comments, however, are (for the most part) immutable in time and directly linked to the textual changes present in the diffs applied in that change.

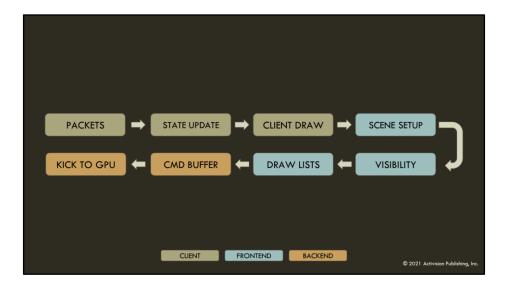
Our approach is that we don't want them to be descriptive or duplicative of the textual diffs in the change--what's the point of restating that you renamed a function when the diff tells you that?

Instead we want to know *why* you made the change you did, CLICK what testing you did to validate it CLICK, and what were the observed results?

With that information, we can go back and test an assumption that was made earlier and validate the results/benefits are the same as they once were.

Which is what I should have someone do later this week on PC!

This is one of my changes from a couple of years back when I still did engineering work, and is based on a style popularized by Robert Field at IW.



Back to our render work submission.

In comparison to the physical CPU and logical thread setup, we can also think of the renderer from the top-down as a pipeline of data and tasks moving across threads and time.

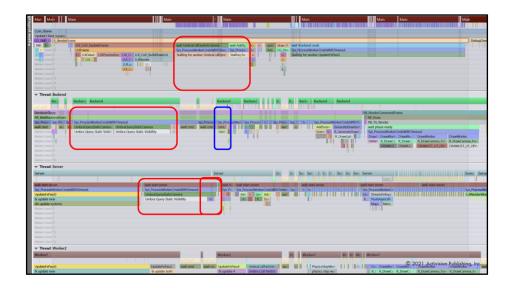
The front of the pipeline is the state updates to the world simulation.

The middle piece is the frontend scene traversal, including scene setup, which includes the view/camera data, light state, game mapping between semantic image resources and actual texture data, etc.

It also kicks the visibility jobs which are then the input to the draw list generation.

The last piece is the command buffer generation and work submission to the GPU, which is owned by the backend.

The draw lists are traversed and turned into command buffer which are then kicked to the GPU in scene submission order.



Zooming in looking at the early part of the frontend, during the visibility phase jobs are kicked per camera (main scene, sun, spots, etc.) and then per object type which determine visibility of these disparate elements for each camera. We have used a variety of visibility determination approaches through time, from classical portals with narrowed traversal, to bespoke software rasterization of likely occluders, to middleware solutions like Umbra.

All of these approaches have varying tradeoffs which we don't have time to explore in detail, although harkening back to my earlier point about re-testing assumptions, we did do a fulsome evaluation of these again on a recent title.

For the sake of today's discussion we'll assume that we're using Umbra for visibility determination.

In our case we don't use Umbra merely to analyze the presence of renderable objects in the world, but we also use it to determine visibility of lights, reflection probe volumes, and decals.

Culling inactive "scene meta objects" is a valuable way for us to constrain overall processing as part of our forward+ renderer.

Here you can see the majority of our time is spent processing static object visibility.

CLICK

And then a much smaller section here where we're processing what we call "scene entities".

CLICK

Two other things worth noting here.

CLICK

Here you can see a small job embedded at the end of the last static visibility query job.

This is a coalesce job that unifies all the per-job buffers into the final list.

We run it inline rather than have another job kicked and waiting in order to keep the memory hot.

CLICK

And here you'll notice a non-trivial inefficiency.

We're spending a lot of time spinning here because our wait is on a job with a long dependency chain and we don't have much else to do, and we're afraid of missing end of the visibility jobs, so we're not picking up other work.

To some extent this is a legacy of the capture I made which is an empty MP match, a normal game would have more work to hide here, but it's interesting to observe, I may yell at someone about this.

But this goes back to the earlier point about concern for bubbles, etc.

Sys_ProcessWorkerCr: UmbraQuery?2a		art server	SI wait start server				_				
intraQuery/Stati-		ocessWorkerCmdsWithTimeout	Sys_ProcessWorker								
	E Dp	vsEntity Dp D;	GenerateVc A	ddSpotShadows		erateFrustumLightBuffer	AddSceneEnt		SoundVoic 5	wait cmd	
intra Fille 3		Urado: R		choose lights	Update UGB Lo: R.U. R.	R_FrustumLights_Process	SceneEntSurfaces		5		
	R	L DOBK					RA IIIII				
		1 xt						_			
Thread: Worker2											
W	Vorker2										
vf_wait										evf_wait	
ait and	UpdateFxPass4			8	UmbraCullParticleSpriteCarnera				\$ \$	wait crnd	
larkor Level	fx update 4			-	Umbra Cull Particle Sprites						
larker Level: 2											
tarker Levet 3										/	
Thread: Worker0)										
 Thread: Worker0 Worker 		0						Wo	iker0		
Thread: Worker0 Worke		a						Wo	nker0		
Worke	erD Worker		Carrier and an of the		avtate/uDaves			Wo	rker0		
Worker of west	erD Worker	Des (AddStaticModeRodCalc R AddStaticModeRodCalc		ApdateFxPass5 fx surfs			Wo	iker0		
Worker alt and Uk taker Levet	erD Worker		R. AddAllStaticModelSurfaces		fx surfs			Wo	iker0		
Worker	erD Worker	Des (Wo	ike#0		
Worker of west	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	iker0		
Worker	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	ker0		
Worker wit cred U tarker Levet 2 tarker Levet 3 tarker Levet 4 tarker Levet 5 tarker Levet 5	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	4xe0		
Worker wit cred latker Level 1 Latker Level 2 Latker Level 3 Latker Level 4 Latker Level 4 Latker Level 6 Latker Level 7	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	iken0		
Worker alt cred alt cred	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	iker0		
Worker sat cred sat cred sat cred satur Level tarker Level satur L	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	iker0		
Worker alt cred alt cred	erD Worker	Des (R. AddAllStaticModelSurfaces		fx surfs			Wo	ker0		

In the next phase, we populate draw lists which correspond to various passes in the scene, and which map back to camera setups and visibility lists from the first phase.

These population jobs are launched according to the specific underlying object type.

CLICK

Brushes

CLICK

static models

CLICK

dynamic models

CLICK

and particle systems.

These draw lists include typical passes you'd be familiar with like shadows, prepass, viewmodel, opaque, but also special buckets like subsurface scattering skin materials, etc.

Each of these draw lists also corresponds to a command buffer chunk which will later be generated based on the draw

list assuming it's not empty. CLICK

You can also see some of the meta objects I mentioned previously like the frustum lights here, which we draw as objects into the bit-vector with atomics.

Custom Sync.		3 - (14.998mi)		
Dude 5: CPUB Main Dude 5: CPUB Worker0 Dute 5: CPUE Worker0 Dute 5: CPUE Work Worker1 Dute 5: CPUE Record	Main Main Worker0 Worker1	Worker0 Worker Work W. Worker0 Worker1 Worker1 Worker1	tein V Wo Worker0 Worker0 V Worker1 Work V V V V	okeit sis sis sis
Autor 1 - CPU 4 Autor 1 - CPU 5	Backend Worker2 Se Server Server	Clacker/l Worker?	Worker2	ackend
Custor 1 - OPU fr ad mix thread		Sour Sound mix thread		
• Thread: Backend Backer Backend	Rackend		lackerst	ackend
enderseSki (no.toTienderCrids eitbicker: (new crids shadow	Fase CLUB SERVICE		10. ever crick eminier	esec post.id
ChocksW Sun the Sun thedrew Map	R.D	R DrawSurf R DrawSurfs R DrawS	Emissive Film	RB_ProcessPostIfied:
ladad Grei Lakar Land Jacker Land				
Anton Const Anton Const Anton Const				
larker Lord larker Lord heiner Lord				

The object draw lists are split up by semantics of which part of the frame they belong to: shadows, viewmodel, opaque, skin surfaces, distortion effects, transparencies, UI, etc.

Rough categories called out here show us submitting CLICK shadows first, CLICK then prepass, CLICK then opaque, broken up into several segments.

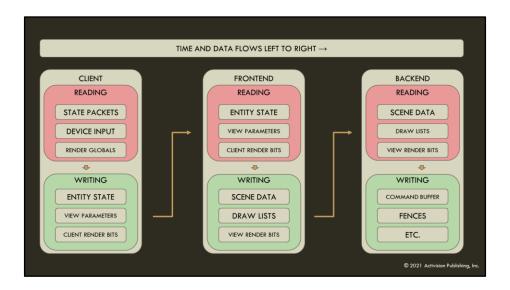
CLICK

Then what we call emissive which is really a deprecated term for transparencies and particle effects.

CLICK

Last, the post processing chain is submitted.

So that's a sort of more granular view of the job flow of each of the pieces of the pipeline but next I want to talk a little bit about data flow.



What I'd like to talk about next is data mutability.

The draw lists are mutable from the frontend's perspective, and immutable from the backend's perspective, modulo many years of small subtle and shameful hacks here and there.

There is a general flow of feeding data forward that is present in a lot of the different stages in the renderer.

We're transforming or copying data, or we're transitioning it from writable to read-only as it flows through the pipe. The split between the frontend and the backend is the major inflection point, with the largest gathered data bundle, the 'backend data', but there are multiple other variations of this throughout the frontend itself as it is forking and joining jobs.

SSAO	AA	GLOBALS MOTION BLUR	SPLITSCREEN	ETC	
CLIENT	0 REFDEF		CLIENT	1 REFDEF	
FOV	DOF		FOV	DOF	
VIEW ORIGIN	LENS		VIEW ORIGIN	LENS	
TIME	SSR		TIME	SSR	
SSAO	FEATURES		SSAO	FEATURES	
FOG	ETC		FOG	ETC	
					© 2021 Activision Publishing, Inc.

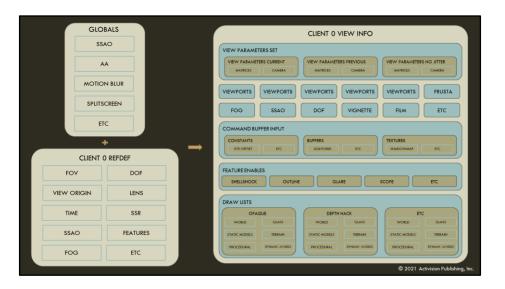
I'll try to illustrate the data flow in more detail and talk about why it is structured the way that it is at each point. First, there is global render data stored in the appropriately named render globals.

Then each client has a set of data that belongs to it specifically, including what is known as a reference definition or refdef.

In the case of splitscreen this will mean two locally connected clients, for instance, and thus two refdef.

Incoming packets from the network tell us how to modulate the client state and then in turn its refdef is updated, as an example the current fog state for the player.

This will be a variety of parameters like Rayleigh (RAYLEE) scattering, etc., that would be interpolated from vision sets that the server informs us are now in effect, etc.



Something we call the viewport features, will be passed down into the renderer frontend which will make a copy and modulate it based on certain renderer limitations that the client need not know about, most of which are debug tunables but some of which represent PC optional features, etc.

We then begin splitting up in the input refdef data into pieces based on frequency of specification, data that needs to be saved as the previous view parameters for temporal effects will be split from others which have no cross-frame references, etc.

We now also begin to setup the "view info" which will be one of the major first class objects visible to the command buffer generation as an input.

As the "view info" is setup, so is something we call the "command buffer input" which contains a lot of input data specific to command buffer generation.

For instance you want to have a system to map render targets as inputs to later passes and you can use this system to specify these.

They're decoupled in this way so you can override them with e.g. black/white/gray images for various purposes, mostly debug but sometimes for real effect in the game.

Similar mechanisms exist for constant buffer management in the "old push style" although we're moving more and more towards arbitrary packed blobs of data in pull style these days.

BACKEND DATA 0			BACKEND DATA 1	
	COMPLETION STATE HEP-X5 EC	COMMAND BUFFER STATE ALLOCATORS RING LINGA SKINCADE MATTIEN	Disker Call CORVER NEWORY DRVCE COMPLETION TIGHT HERSEL TESEELATION ASTINC REVES	ec
			BHOTS	
CRAW USDS CHILD	IC GASS STATE INCIDES HOCEDURAL Presser incides	DRAW USTS CFACUE WORD GLASS HAIN MOORS POLIEURAL PRAMM MOORS	DEPTH HACK DEASS WORD WORD STAIN: WORD DEASS WORD STAIN: WORD STAIN: WORDS TERAM MOLESARY MOLESARY FROCEDURE IPHAME-KORDS FROCEDURE FROCEDURE	K GLASS TREAM Primark MORE
		IME		\longrightarrow
FRONTEND WRITING 0	FRONTENE	WRITING 1	FRONTEND WRITI	NG 0
BACKEND READING 1	BACKEND	READING 0	BACKEND READIN	4G 1
			e	© 2021 Activision Publishing, Inc.

We'll also begin setting up "backend data".

The backend data represents the interchange between the frontend and the backend of the renderer, namely the difference between scene traversal and command buffer generation.

There are two of these "backend data" structures, and as soon as the frontend thread is done writing it will be handed off to the backend as transfer of ownership and then the frontend thread can immediately begin writing the next frame's worth of data.

This double-buffered pipelining allows us to amortize and overlap work at the cost of some carefully managed latency. Another thing worth mentioning is that we try to avoid is any linkage in saved state between the frontend and backend. There is little caching of work in the backend, everything is feed forward to avoid consistency issues in such designs, and the requisite synchronization.



We discussed the duplicated backend data for pipelining previously.

In the case of splitscreen we'll also utilize this double-buffered mechanism to begin processing the next "backend data" for the second viewport.

CLICK

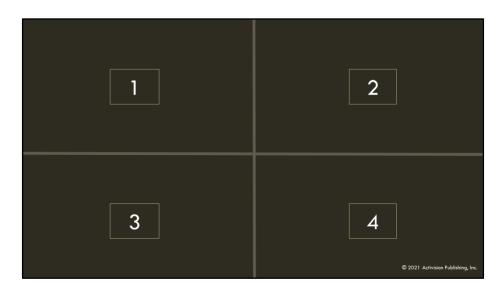
This reduces our pipelining benefit of course but given the additional stress on systems to support splitscreen it works out alright in balance, and it is most important that we generally have a fully independent set of resources available for the second viewport when possible.

CLICK

Unfortunately our support is not as elegant as one might hope, but of course elegance and efficiency aren't necessarily bedfellows.

It's not uncommon to find various rendering systems disabled to improve performance in splitscreen such as tessellation (even worse quad efficiency at sub-viewport!),

Moreover there are some systems whose benefits simply don't exist, for instance early asynchronous compute jobs which expect "end of frame" work to overlap can collide with full cost end of viewport draw work from the first client. Or, due to memory lifetime management, would need to stall for longer than would be desired in order to have correct CPU/GPU synchronization.



Last, some systems can be optimized based on previous frame hints, such as our lightgrid spatial walk, and this is more complicated to manage in splitscreen.

CLICK

Some work is amortized--we'll often do tricks like only render one set of sun shadow cascades for splitscreen.

CLICK

A lot of the functionality that is currently encoded in splitscreen is a mixture of past legacies as well--picture-in-picture, stereoscopic rendering, etc.

CLICK

But splitscreen is still important to the franchise and players and we do the work to support it even if it's not internally as beautiful as we'd like.

CLICK



In the case of past features like stereoscopic 3D...

CLICK x 8

... we managed to get up to 8 backend data swaps per frame, which, uh, mostly held 20Hz for four player co-op stereoscopic 3D.

I'm guessing probably a dozen people ever ran the game in that configuration.

	INDIRECT ARGS [0:17]				SURFAC [18:		
SURFACE INDEX [32:35]	TESSELLATION [36:36]	PREBAKED [37:37]	TESS FACTORS [38:38]	PREPASS FLIP [39:39]	FORWARD FLIP [40:40]	COUNT [41:57]	UNUSED [58:63]
						© 2021	Activision Publishing, Inc.

Another benefit for mutability analysis is that if you can freeze the data early in the pipeline you can avoid copying overhead and just refer to it.

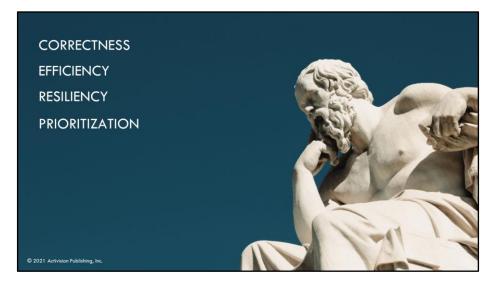
And copy overheard is something to pay attention to, and to make decisions about bulk vs fine-grained, data structure size, etc.

Which is why for example our drawable interchange data is very referential, and tightly packed.

Take for example our world-geometry drawable surface data.

It's encoded as a 64-bit integer.

That surface index refers to other arrays of data in true SOA spirit, but that data is similarly packed, albeit it into 32-bits, and another chunk which is 32 bytes, etc.



Let's pause for another philosophical interlude on what I consider some of the most important framing ideas of our renderer design, which are correctness, efficiency, resiliency and prioritization. [SOCRATES]



First of these, minimize explicit synchronization needs to ensure correctness.

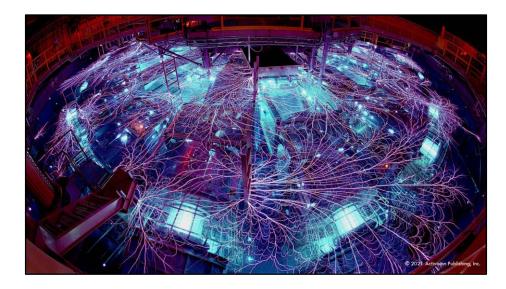
This implies a fair amount of copying, and one of the criticisms of our internal engineering team is that it can take a lot of effort to move things through the render pipeline.

CLICK CLICK CLICK CLICK

But this data stays immutable as it flows through the pipe other than the copy points and thus minimizes synchronization needs, complex mutex systems, etc.

As well as the tendency for bugs to creep in due to referencing memory we shouldn't, similar to what we spoke about earlier with the client vs server fence.

This isn't a new or novel insight per se, functional languages have talked about this for years, but we have seen a lot of messes from state mutation, so discipline here really pays off.



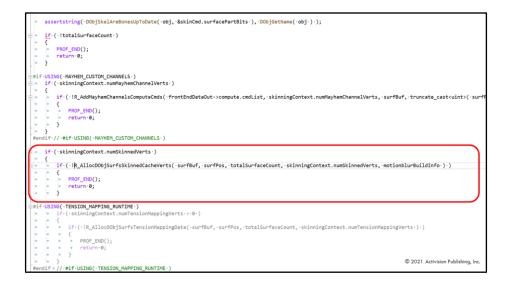
The downside of these multiple stages is of course their cost.

Thus our second goal, which is to minimize the amount of data that needs to flow through the rendering pipeline as much as possible for the sake of efficiency.

A more general way we would sometimes say this is that our goal with code and data flow is to minimize total data transformation energy.

Each transform you are required to do, whether a copy or calculation, represents the spending of some finite resource in the system such as memory bandwidth or ALU.

Minimizing this keeps the renderer efficient.



I also discussed that we valued resiliency.

We want our designs to be resilient to unknown inputs at a level that is appropriate for the constraints we have chosen. Tools should never assert on malformed input, for instance, but handle errors gracefully and report as much context as possible to the user.

Similarly if we are unable to determine a static limit a priori, we want runtime systems to handle that gracefully.

As a simple example, one thing we try to do in our traversal that is worth highlighting is that we have a system whereby the frontend is responsible for resource allocation used by the subsequent workers.

This includes determining the buffers necessary to be reserved in the interchange data structure between the frontend and backend.

At the top level these are surface descriptors, but they can also include reserving space in the skinning output buffer or other graphics related structures that are used strictly by the backend.

If we are unable to reserve this space we simply elide the draw at the top level--it's a goal of the renderer that it is resilient to heavy load without crashing.

This is especially helpful in the early days when most gameplay was 6v6 DM and then a killstreak would come in and try

to draw the entire map at once and things would still be handled gracefully.

This is our top level "dynamic model" draw code, which if it has skinned verts will attempt to allocate them and simply skip rendering if it can't.



Inside that code a call to another sub function to get the offset into the skinned cache buffer, and skip if unable to allocate.



CLICK

The actual code to alloc from the buffer.

CLICK

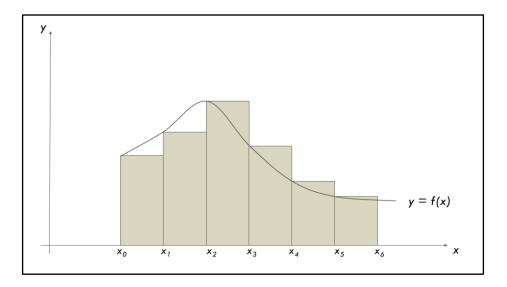
The atomics.

CLICK

And if unable to allocate, issue a warning once per frame, and then return an error.

Another important part of this resiliency is validating it--one thing we encourage programmers to do as part of their testing for checkin is to artificially reduce their limits and ensure system works correctly.

In this case we'd ask them to knock down the size of the skinned vertex cache vertex buffer to something trivially small and see what happens.

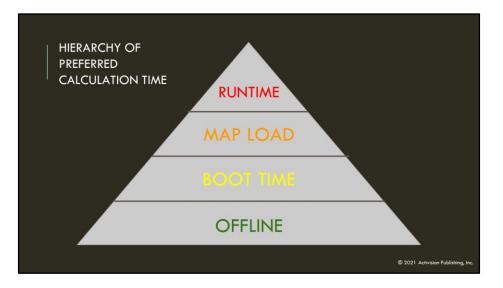


Last, prioritization.

This is the idea that you should gauge the value of a change by the area under the curve of its exposure to the players over time.

If you're working hard to optimize a part of the code that can be very slow but isn't seen very often, an occasional spike, the area under that curve of the player's experience is pretty small.

However if you're talking about the tradeoff in a lighter needs to wait an extra 45s for the radiosity solver to finish but the new output will result in a savings of 130us each frame and multiply that out by the number of play hours... you get the picture.



This also leads to a sort of corollary which is the "hierarchy of preferred calculation time", which Angelo so viciously slandered.

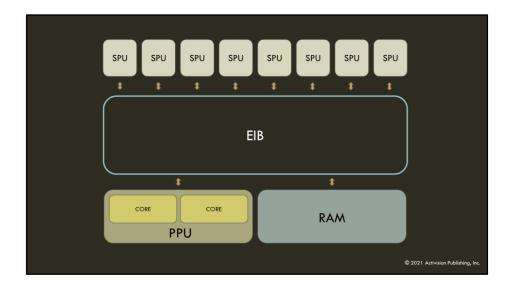
Let's consider resource allocation within the frame.

Because we value correctness and efficiency we try to disallow anything that looks like dynamic memory allocation during the frame--we want to frontload calculation of necessary resources to as early as possible.

In the best case we can do that offline by reserving memory for some known upper bound across all of our levels.

At boot time we may make decisions such as which piece of hardware we're running on, resolution of the output device, etc.

Then at map load time we can make a sized allocation per specific level based on its contents or game mode. And then at runtime we want our allocators to be as simple as possible, which means general some form of linear allocator with a per-frame reset.



Another thing I would be remiss if I didn't bring up.

These philosophical positions exist not just because of some first principles formulation that they were considered good and valuable.

Some of them exist because of very strict platform limitations imposed in previous generations, such as the PS3.

For those who don't know, the PS3 had many small processors with very little memory and you had to be very careful in how you explicitly copied memory around, and how small your programs working set was.

But ironically this approach didn't just make the PS3 versions of our games possible.

It also made pretty much everything faster.

That's because data locality and organization matters.



There's a quote from Frank Herbert's Dune that I sometimes think about when considering the impact Cell had on how we think about programming, at least how it shaped my approach.



And that's the end of our philosophical intermission, let's get back to the frame...

54ms	55ms	Sbms	57ms	58ms	59ms	60ms	61ms	0.cms	63ms	
Custom Sync					3 - (14.998ms)					
Cluster 0 - CPU 0 Main	Main	Main			Main	Main			Main	
Cluster 0 - CPU 1 Worker0	Man		Worker0	Worker0		Wo Worker0 V	Min Moderal	Wednet	Worker0	
Cluster 0 - CPU 2 Work: Works	el.		Worker1	S Worke		Worker1 W		Work V. V.		
Cluster 0 - CPU 3 Backend Cluster 5 - CPU 4		Backend		Backe		Backend			Backend	
Cluster 1 - CPU 4			Norker2			Worker?		Worker2 V. V. Worke	12	
Cluster 1 - CPU 5	Se	Separat	Server	Server				C1000000000000000000000000000000000000		
Cluster 1 - CPU 6 ad mix thread	3	50.00	1000		Sour Sour	d mix thread S S S			a constant of the second se	
Cluster 1 - CPU 7	5				10 20020					
					CONTRACTOR OF CONTRACTOR					-
Thread: Main										
	and the second second	_						_		
Thread Backend				-						
										_
linke Backend		Backend		Backe		Backend			Backend	
			100000 1100							
RendererS ExecuteRenderCrm wait backet exec crnds st		1 Inve	cmds opaque				_	R8/ exec ands emissiv	e exec post3d	
R Process	n Shadow Map		(Prepa:	Opaguetter2	Opaqueller3 r Opaquelle	4 OpequetterS		Emissive	R8 / R8 Process	PostEffects
R.D. R.D. R			R.Dr.	R_DrawSur	R_DrawSurfs R_DrawSu	fs R_DrawSurfs		emissive		
Marker Level 4								R_DrawSurfs		
Marker Level 5)			
Marker Level 5 Marker Level 7)			
Marker Lever										
Marker Level Marker Level: 2 Marker Level: 8 Marker Level: 9										
Marker Level 2 Marker Level 2 Marker Level 8 Marker Level 9 Marker Level 10)			
Marker Lever 2 Marker Levet 2 Marker Levet 8 Marker Levet 9 Marker Levet 10 Marker Levet 11										
Marker Lovel Marker Lovel 2 Marker Lovel 9 Marker Lovel 9 Marker Lovel 10 Marker Lovel 11 Marker Lovel 12 Marker Lovel 13										
Marker Level 1 Marker Level 2 Marker Level 5 Marker Level 5 Marker Level 10 Marker Level 11 Marker Level 13 Thread: Server							י ר			
Matkis Even Matkis Event Matkis Event Matkis Event Matkis Event Matkis Event 1 Matkis Event 2 Matkis Event 3 Thread: Server	1 1		Worker0	Woked) Worker: Work	W: Worker0 V	Wie Worker0	Worker0	Worker0	
Mahar Dong Mahar Kond Mahar Kond Mahar Lond Mahar Lond			Worker0			We: Worker0 V	Min Workerd	Worker0	Worken0	
Manua Cara Manua	w draw	cherr	Worker0	Worker0	dii draw		i ound a physiphy	ics step collide physic in Ind	col umbra di Chi ci i i cen	e static 1 urri
Marine Long C Marine Long C Marine Long C Marine Long C Marine Long D Marine Long D Marine Long D Marine Long D Thread: Server Thread: Server Thread: Server Workwol W. W Workwol Marine Long C Marine Long D Marine	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Woke®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	ii oxedicii physi physi physi Cier	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	il static i seri Um
Matha Loor Matha Loor Matha Loor Matha Loor Matha Loor Matha Loor Matha Loor Developer Thread: Server Thread: Server Worker0 With WWorker0 With Server	w draw	cherr	Worker®	Worker0	dii draw	ynLightes	Cler Ger	ics step collide physic in Ind	col umbra di Chi ci i i cen	il static 1 sum Um
Marine Long C Marine Long C Marine Long C Marine Long C Marine Long D Marine Long D Marine Long D Marine Long D Thread: Server Thread: Server Thread: Server Workwol W. W Workwol Marine Long C Marine Long D Marine	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Worker®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	ii oxedicii physi physi physi Cier	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	8 static unit
Manna Lawr Madrau Lawr Madrau Lawr Manna Lawr Manna Lawr Thread Thread Workerd	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Wole®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	E static 1 Gunt
Market I ware i	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Woke®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	E static 1 sam
Market Level (Market Level (Market Level () Trends Score () Your (rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Worker®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	i static umi
Mainto Longio de la conservación	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Worker	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide reference: Table Collide Col	cd, umbra c) CCCCCC ce	e static – seret Um
Material Constraints (Series 1) States Long (Series 1) Material Series (Series 1) Threads Series (Series 1) Threads Series (Series 2) Threads Series (Series 2) Threads Series (Series 2) Series (Se	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Woke®	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide physic in Ink ItBavoki Collid Chill D Dy	cd, umbra c) CCCCCC ce	f static 1 som
Mainto Longio de la conservación	rw draw pass Prepass Opaqueiter4	draw Opsquetter1	Worker	Worker0 draw Opsquetter4	dia draw Tri Trans/C	ynLightes	Clear	ics step collide Historic II Collido Carlo College Tasi Den II Do II Di	cd, umbra c) CCCCCC ce	Um

So getting back to the frame.

I mentioned before that originally the backend didn't just submit the command buffer but was also responsible for generating it.

However in the seventh generation of consoles (Xbox 360, PS3) this became multithreaded parallel command buffer generation.

CLICK

Here you can see the "Worker0" thread picking up command buffer generation work

CLICK

While the backend is also helping and kicking.



Because the scene graph traversal is divided into jobs and they can complete at a variety of times, but the backend submits work in a specific order, we want to minimize latency and bubbling by ordering our work submission and kick-off as strictly as possible.

To accomplish this the frontend/backend interchange data is actually partitioned into subsections which are fenced so that they we can see when the previous frame's backend is done reading from it, and beginning writing to it from this frame's frontend.

The worker commands that generate the command buffer packets are actually launched from the frontend as their respective scene graph traversal completes, and then these are waited on by the backend before they are kicked to the GPU.

As such, the backend does not wait for the frontend to finish processing the entire scene graph traversal, it instead kicks work opportunistically.

CLICK

So here we see that one of the frontend jobs is signalling that we have made sufficient progress in the scene graph traversal to wake up the backend, which unfortunately picked up long job, some work for us there to improve scheduling

heuristic.

CLICK

And here we can see a variety of worker commands picking up draw workers before the backend has even woken up. CLICK

And now we're actually getting around to kicking command buffers to the GPU on the backend, and those jobs are already done so it's just rapid fire kicks one after the other.

Marker Lovel 9					
Marker Level 10					
Marker Level 11					
Thread: Backend					
Backend		and the second se			
Backend		Backend		Backend	
				l II	
RendererSleep					
ExecuteRenderCmds Msrd Leve R exec cmds opaque					
Marker Level 3 num rur ru num n. n.			IN DRAWLIST LIT OPAO	AN (11522)	IN DRAWLIST_LIT_OPAQUE_ITER4 Nun DRAWLIST_LIT_
Marker Level 4			R DrawSurfs	00100	R DrawSurfs R DrawSurfs
Marker Level S			- General		
Marker Level 6			User Marker (Event		
Marker Level 7			Name	run omdbuf: DRAWLIST_LIT_OPAQUE_ITER3	
Marker Level 8			Stack Level	3	
Thread: Worker1			Inclusive Duration	91.576us	
Worker1		Warker1	Exclusive Duration	82.008us	
Wolker		WORKER T	Inclusive Time	91.576us	
1200-000	1120		Exclusive Time	82.008us	
LitOpaque DRAMUST LIT OPMOUE	DRAWUST LIT OPAQUE ITER3		Color	0xff20a5da	EntityUpdate EntityUp: EntityUpda Entity
R.DrawSurfs	R DrawSurfs		User Marker (Rang	ed)	NS1 exec ent work exec ent i exec ent w exec
Marker Level 3	P		Calls	1	
Marker Level 4			Total Time	91.576us	
Marker Level S			Total Exclusive Time	82.008us	
Marker Level 6			User Marker (Globa	n an	
Marker Level 7			Longest Duration	99.270us [Frame 1]	
Thread: Worker2			Shortest Duration	66.729us [Frame 4]	
Worker2 Worker2	Worker2			Worker2	
Worker/	WORKER2			Workerz	
LitOpaque DRAMUST_UT_OPAQUE_ITTER2		DRAWUST_LIT_OPWQUE_ITTR4			Life EntityUpdate EntityUpdat EntityUpdate E
R_DrawSurfs		R DrawSurfs			Di exoc ent worker exoc ent w exoc ent w exoc ent
Marker Level 3		n jorampans			
Marker Lovel 4					
Marker Level S					
Marker Level 6					
Marker Level 7					
Thread: Worker0					
Worker0	Worker0	I I I I I I I W W V W V W		N Wo W Wos N Wo Works W	W. Works Works 3 Wo N M Works
TO DO DO	TO ALL			THE REAL PROPERTY OF	THE PARTY OF A 1 WORK
		(Whenever			Collected and the second se
1 Marana		UROpaque	IE (TERS		EntityOpdate I essec ent worker
LROpaque					
DRAWUST_LIT_OPAQUE_ITER1		DRAWLIST_LIT_OPAQ	101010		EARC ERS WORKING
		R_DrawSurfs			
DRAWUST_LIT_OPAQUE_ITER1 R_DrawSurfs					
DRAWLIST_LIT_OPAQUE_ITER1 R_DrawSurfs Marker Level 3	-				© 2021 Activision Publishing,

In addition, if the backend notices that draw work is coming in late, possibly due to poor scheduling behavior of worker commands, it can perform something we call handoff.

This is where we interrupt that worker command and kick what work it has completed, and then continue with the draw list on the backend itself and submit it to the GPU as quickly as possible.

This reduces GPU bubbling, that critical path I mentioned previously, and drives down overall latency.

That said, our double-buffered pipeline scheme itself introduces latency, and we have a variety of systems that attempt to control this.

CLICK

Going back to handoff and showing it on this frame.

CLICK

Here you can see worker 1 was processing the fourth segment of our opaque draw list.

Because our opaque draw list generally tends to be the longest and heaviest we break it and the corresponding prepass list into segments for better job granularity.

CLICK

This small job here is where we try to run it, or kick it to the GPU, but we find it is not ready to be submitted so we get impatient, flag it, and the worker command stops processing.

CLICK

And now here you can see that same segment picked up by the backend and then submitted directly.

Markor Level 12				
Backend				
<razordummymarker></razordummymarker>	R8_RenderCommandFrame			
<razordummymarker></razordummymarker>	RB_Draw			
Sys_ProcessWorkerCmdsWithTimeout	S R8_TG_Render			
GenerateFxVertsUmbra		87_potShadow_DrawStatics_68		RBT_Spot
X_GenerateVerts /fx.draw.sprites		run amdbuf: DRAWLIST_SPOTSHADOW_0_STATIC	cnd: c	DR/ run
tx onew sprites		slow wait	C DOL C	
	in the second se	wait drawlist DRAWLIST_SPOTSHADOW_0_S	TATIC	
		and a second colored and a second sec		
		-Departmenter		
		R_DrawCarnera_Forward		
arker Level 11		DRAWUST_UT_OPAQUE_ITER1		
arker Level. 12		R_DrawSurfs		
irker Level 13				
Thread: Server				
ver				
IVER: Frame				SERVER: Frame
VER: run frame		SERVER: post fram	e	SERVER pre frame
Frame			endClientMessages	SV_ClientMP_FinishExtrapolatio
tunFrame	nframeForEntity Scriptat: Clienti SV wait Ex		F_SAVE_A^ wait BuildCur wait Archive	wait ExecuteClientThink
		ecuteActiveClientEn: ocessWorkerCmdsWi	Sys_Process\ Sys_Process\ BuildCurrent ArchiveSnap	Sys_ProcessWorkerCmdsWithT ExecuteClientThink
ker Level S ServerPhysicsWorlds		eActiveClientEndEra	SV_SnapWor SV_Snapsho	G ActiveMP ClientThinkIntern
kar Leve S S Serv S ServerPhysics S ServerPhysic		Ent BG.P. Ph	SV. BuildSer	G.Pmove
ker Leve ServerPhysic G physics upd		BG.P	and the second sec	ProveSingle
ker Level 9 ServerHa S Sc ServerPhys				PM P PM Normal Update
		•		
cer Levet 10 Generati C ServerPh		•		PN PM_Ground PM BC PMove Tr: Ph
ter Lavet 10 Generate C ServerPh Se Serv C ServerPh C S				PN PM_Ground PM_ BC PMoveTic Ph P1 PhysicsQu P1
or Levet 10 Connerati C Server/This or Levet 11 Sr Serv or Levet 12 or Levet 13				PN PM_Ground PM BC PMove Tr: Ph
or Level 10 Generat C Server71 or Level 11 Sc. Serv				PN PM_Ground PM_ BC PMoveTic Ph P1 PhysicsQu P1
or Level 10 Connect C Server17 or Level 11 S. Sorv or Level 12 a Level 13 ar Level 13 ar Level 14 or Level 15				PM PM,Ground PM, BC PMove Tro Ph P1 PhysicsQu P1
ar Lovel 10 Generate C Server 11 and 12 Server 11 and 12 and 12 and 12 and 12 and 14 and 15 and 16 a				PN PM_Ground PM_ BC PMoveTic Ph P1 PhysicsQu P1
ar Lovel 10 Generate C Server 11 and 12 Server 11 and 12 and 12 and 12 and 12 and 14 and 15 and 16 a		•		PN PM_Ground PM_ BC PMoveTic Ph P1 PhysicsQu P1
ins Level 10 Generation (Second 10				PN PM_Ground PM_ BC PMoveTic Ph P1 PhysicsQu P1
ins Level 10 Generation (Second 10				PN PM_Ground PM_ BL PMove Tr: Ph. P1 PhysicsQu P1
ter Loud 19 Generati (L) Second 19 Jan 19 S Second 19 Jan 19 S Second 19 Jan 19 S Second 19 Jan 19 S S S Second 19 Jan 19 S S S S S S S S S S S S S S S S S S			muttoriar	PN PM Ground IPN PMore N. PA P Physics, P Physics, P Ph
to trust 0 Generati () Severit to trust 0 S. Sev. to trust 0 S. Sev. to trust 0 S. Sev. trust 0 S. Sev. To S. Sev. S.			DrawCamera Forward	PA DATA PARA PARA PARA PARA PARA PARA PARA P
la cura ti Connecti Sanachi la cura ti Si Sana la cura ti Si Sana Intrast Wakerl Anna Connectional Si Connectional Sinanci Si Sanati Intrast Wakerl Connectional Si Connectional Sinanci 2 & 2,0 and 5			DrawCamera_Forward RAWLIST_LIT_OPAQUE_ITER3	PA Const. PM. B PMontili PM Physics P Physics Physics P Physics Physics P Physics P Physics Physics P Physics Physics Physics P Physics Physics Physics Physics Physics Physics Physics Physics Physics Physics Phys
ter sour d'anne de la constant de la			DrawCamera Forward	PA Const. PM. B PMontili PM Physics P Physics Physics P Physics Physics P Physics P Physics Physics P Physics Physics Physics P Physics Physics Physics Physics Physics Physics Physics Physics Physics Physics Phys
landerfin de landerfin de landerfin de landerfin de la			DrawCamera_Forward RRWLIST_UT_OPAQUE_ITER3 R_DrawSurfs	Ph. (Process), Col. Ph. (Process), Col. Ph. (Process), Col. Ph. (Process), Ph. (Ph. (Ph. (Ph. (Ph. (Ph. (Ph. (Ph.
la cura ti Connecti Sanachi la cura ti Si Sana la cura ti Si Sana Intrast Wakerl Anna Connectional Si Sanachi Sanachi Connectional Si Sanachi Sanachi Connectional Si Sanachi			DrawCamera_Forward RRWLIST_UT_OPAQUE_ITER3 R_DrawSurfs	PM PM_Ground PM_F BC PMoveTic PM_F PI PhysicsQu PI

In what is now becoming a theme, as I was putting together this presentation I noticed an inefficiency in the handoff system.

CLICK

Here you can see a very nice tight set of kicks of command buffer.

CLICK

Then we get to our cached spot shadow static draws.

And we start waiting... and waiting.

CLICK

Specifically on this worker down here which is taking its time.

And so we initiate the handoff mechanism.

But interestingly it can't get the draw on the worker to interrupt!

CLICK

Instead the backend picks up a random opaque iterator so as to do some work.

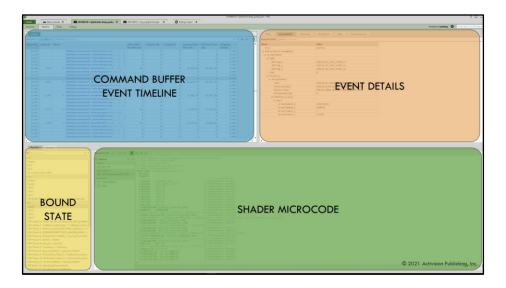
I think this is due to the granularity at which we exit our tight inner draw loops, which are highly optimized for minimal

possibility of state change in the inner draw loop.

As we've moved to more of a bindless and multi-draw indirect approach with unified shaders, we have continued to coalesce the granularity of our inner draw loop.

We're simply not breaking out of it frequently enough to check if we should pause our work.

Something else to have people look at later this week.



A new analysis tool appears!

For some of this command buffer generation, a quick introduction to PIX3 for those who haven't used it before.

CLICK

On the top left, a vertical list of events recorded in the command buffer such as draws, dispatches, resource sets, state sets, etc.

CLICK

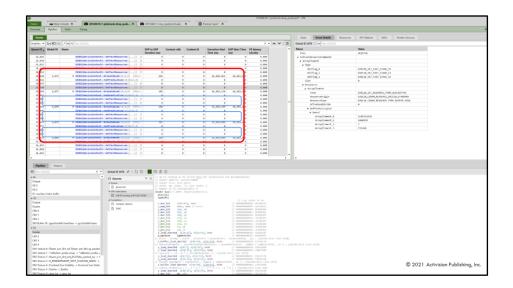
On the top right, any details for that event, you can see this resource set is for a buffer object, namely an updated constant buffer set.

CLICK

Bottom left, details of what is bound on the device at the time of this event.

CLICK

And then what I used to spend a lot of time looking at, shader microcode.



Back to our analysis tools.

I mentioned earlier that we have customized inner draw loops.

This sort of specialization has long been a feature of the renderer, the idea of having these very specific types of drawable objects.

Roughly speaking these are world, static models, dynamic models, procedural geometries such as particles, glass, and in newer titles, terrain geometries.

Each of these types has specific constraints, and also has specific draw code that goes alongside them.

In the seventh generation of consoles, the Xbox 360 and the PS3, that draw code was setup to minimize state changes, which in that era meant also just minimizing the surface area exposure to the rendering API.

We accomplished this with gross material/asset types, and then at data serialization time we would calculate a much more fine-grained key based on the various draw state requested for each material.

This key allowed us to quickly sort assets to minimize those state changes, and was a part of the frontend surface emit phase.

On the backend then this resulted in very optimized command buffer generation for those platforms.

CLICK

Here you can see just two simple constant buffer updates between some draws.

CLICK

This setup does impose a lot of rigidity, though.

For instance within the static model draw loop you couldn't change anything except the reflection probe index and secondary lighting data index, along with the actual instance data itself.

When we went to later add vertex-based baked lighting, and then lightmapped static models, this meant plumbing through a lot of code for those specializations.

			naturti dag godi ji 🖉 🗖	1 20168994 4 es	reșa naistean p					na cese vera divers											
	Papeline	keels Delarg								State				API Objects 140s							
Events											Eyeed			AFI Okjects 1470	Shad	ler Sournes					-
shic .	See CO (Se	* 4a[26]7[](Ber (Co(+1))							× · · · ·			ter • Her C									
anne 1D .	Giobal ID	Name		EOP to EOP Duration (m)	Context rolls	Context id	Execution Start Time (re)	EOP Start Time	PS latency (checks)	10 M	1 Object	Address	Size	Format ALBUIDEDR_FLORE	Width 1,000	Height	Depth	Mip Count	Array Count	Sample Court	
5,291		Settredication v		CONTRACT (SIL)			1000 000			4.0		Dalificianity	26,384	BUR OWSC	200	11.1		1			41
5,292	1.65	# w/ourspa_some_sista_B					6 6,557,796	7,567,833		6 10	(4)	DAUDIALCONS	6,200,768	DRE FLOAT	1,000	104	1				1
5,211	648	# service_simple_ster	(Aug.vol_tint_r_lin_art.004				0 0.557,798	7,567,811		1.93	174	01010-000	134,588	NRCH_HTELE_STENCEL	244	117	1	1			
3,234			3N7,364,83052,9NE38537208,3		6		6 6		0.000	12 · 13	1413	\$51412:9900 0x2004427-00	214,204 5,578,348	UC/_UNDER	114	314	118				1
3,235		PSd: v22b0dir#	imple fog vol art.eprophere						0.000	17 11		01209512000	2.705.380	34,71047,8981	114	73	1.18				1
3.210			imple_rog_vol_art.mrtarrarrar		-				0.000			Subjectaneous	2,007,113	204 18081	1,874	1,414	1				1
5,218			HISTOR) [TODA->COUDLOGRAPH		-				0.000	- 32 05	5432	DATACAMPERS.	45,534	BUDE HERE	528	528		1			-1
8,230			oristoriable(1,ptr: minute						0,000	- 76 + - 40 -		0x3164280007	2,007,152	15_34094_8291 307_184084	1,834	1,824			-		-11-
5,240			sstantBuffersten;)1, norman				•		0,000	54 52	SALIRS	dalahit harmy	1,907,440	A& UDAT	1,000	964	1				1
9,241	141		edite, c. stration, s. at. / this		<u> </u>		6.357.798	2,187,811	0.000	55 (3	(#2	04398224909	6,395,768	R13003808_FL047		458	1		1		4
5,243	- horas			21					0,000	- 56 g		Carlos Manager	452,752 3,507,668	ENERGY SH CH	800	+18					1
5,245	641-652	# w/vurips new visits it	p t fog vol tist r lin art.dde	21			0 6,557,684 0 6,557,684	7,167,820		- 22 18 58 10		0x101140000	818,752	100,00000		104	- 1				-11
9,240			1487, 112, WHELZ PRESSATION C				e e, esr, eas	7,180,400	0.000	50 0	10	OCLUTION	5,510,310	REGEREAR LACET	1,000	104	1				1
3,247			erigterfeble(1,p(r) month		1 1				0,000	82 14	201	11,227,177,007	1.705.191	11_00097_5093	1/4	15	1.18	1			1
5,240			atattadferstes 10, homeon						0.260	92 -		CAN, AAAAAAA	88,328	a uncertainty axes	236	234					- 51
5,245	640		ad 17,8,110000,8,0) (9360		6 (A		6 6,557,684			94 (1	10.00	ES-90,0014,000	2.097.152	DES LADER	1,014	1.414					- 11
5,258	1.67	[firms]					6 6,957,464	7,167,839		25 03		043385,/9999	65,585	BRIN HELLS	325	1.18	i				1
5,01	110		(M.C., LOS, DEDIL, PHEDELATEDA_C (M.C., L., LINNER, 4., 4) (M.L.						0.000	88 1		EXTLIPTATE2	526,398	\$4. UNIONE ADDES	912	912	1				1
5,214			Mar., 108, 00002_PH000181008_C	-	-		6 6,558,333	7,367,80	0,000	282 +		EXX JIEROS	524,288	10_UNION_ROOL 10_UNION_ROOL	942	312		1			- 1
3,20	10.0		ad 16, 1, 1116/94, 8, 8) (this	-	-		6,008,076	7,167,858		388 -		Particular and	524,288	14 UNCON ADDI	913	912			-		1
5,257			MART, 3.54 , BUDGE PARTICIP. C.				0 8		0,000	111		OCTLABARD	524,368	14. UNCORE ADDI.	51.2	162	÷.				10
\$,210	652		(th) (th) (this are a constructed and	3			0 8,335,264	7,167,857		158 -		Achildenne	\$24,368	14 UNDER X001	113	112	1		1		
5,200		SetPredication(%)	LADOR MULTING, MULTING, DV. 60				e e		0.000	327 +		242003083002	524,288	14 UNDER ADD.	912	512	1				1
3,261		A stindard come states		_				1 101 000	0.000	124 -		Collineaning	534,384	to include another	512	512	- 1		-		-11-
8,289	654	b/europe blow visità il biologica da la statta il					0 6,560,554	7,167,868	0,000	100 10			2.007.354	410_10NI	3,624	3,424	1				1.1
5.279		Tratic (norw)	1110 Lans and researchers.		-				0,000	201 10		exceletaneer	2,017,117	1001_018	1,628	1,424	1		1		1
5,270			1) (Whis-ADDULISemphication						0.000	382 00 J83 00		Constanting of	2,097,132	REALING THE REAL	3,424	1,424					- 61-
5,240			erroration subboosmon, hi						0.200	A41 52	A 10	Calleng process	2,007,152	205_15097	1,424	1.414					- 11
5,281			37, Buffertmation: Bubbbbbc						0.000	245 10	10.00	TO ADDREET TRADE	131,072	8008,97215	128	128	1	1			1
1,282		(loseCumandList()				0	• •		0,000 -	200 -			524,289	34_secord_even	944	112		1			*
Pipelae	Outer	4																			
Per JOI	i+n		Godaal 10 649 1/2 1 () ()		0																
			Fi Sources	* × // 10		4 (123)4 <u>here</u>	for instruction	et doometers	in)												
erja.e			+ losses																		
10			C POR Married				1.20														
¢.			< GPU Instructions	shade	main // Jaren																
1000			1 ITT PE survive a PE (OD M	m 251	C(CE) #(P5)																
eges.			# Compilation						atota in an												
	of contactful	and a low second s	B Compiler Options	3.5	per 164 1, 10 per 164 perci,	AP), care															
	it contavilut			1.0																	
	a commented					14, municipation			noner: destroyer asis												
				5.8	ew 1432																
and a l					ev 137 134,	Include California															
	accester and	Ret-Fa Linear		4.34	offer load detry		. 40435														
	aropa union v			1.1	ook_Lig co, t out_merdat s[-	warth, starst.	1000		NARY: CHEMINA												
tr herbure									ment hat you will done had a								© 2	021 Act	ivision Pu	blishing,	inc.
		ly soldler 2			out therefare site																

This is a similar setup for our world draws, albeit in D3D12 now, to give you a slightly different API flavor. CLICK

Here you can see there is nothing between the draws but predication--our world materials are in, well, world space, so they don't even need transforms from local.

The supported modulation of reflection probe and lightmap index don't come into play so it's just pumping out draws.

nine	Pipeline	Console X 20210422- Tools Debug	I-mp_royal_hoteLxpix X	O Timing Lapla*	*									
Events	1.90.000	Not Cong								Event Details	Resources			
	_	C 1 fing 1							State			API Objects	Edits	Shader Sources
phics •	Tree C Flat	• Ag 16 3[] Filter (Ctrl+E)						ו]••	Global ID 148	H I Aa Filter (Ctrl+				
eue ID	Global ID	Name		EOP to EOP	Context rolls	Context id	1.	🗄 Event List Counters 🛛 🗸 🗙	Name			Value		
				Duration (ns)			т	# Counter Sets	this			I00012Graphic	sCommandLis	t
4,787	1,478),1,1,20,0b5#1302,1500,MURL,4					Context roll 🕯 🔹 🐨	# Indirect					
4,789			<pre>iptorTable(0,ptr: 0x0000cf74; iptorTable(3,ptr: 0x0000cf74;</pre>					+ Filter	4 Arrays					
4,798	1,479		<pre>>ptortadle(s,)ftr: du0800c175e },1,1,20,ob)#1302,1520,MULL.6</pre>					Aa .* Fiter (Col=1) X •	Type			D3012_INDIREC	ARGUMENT_	TYPE_DRAW_INDEXED
4,793	1,479		<pre>iptorTable(0.str: 0x0000cff5c</pre>					# Countern		andCount		3		
100	-		iptortable(3,ptr: execution				 I 	Command List ID	Rytestri			20		
1,795	1,480),1,1,20,00(#1302,1500,MULL_4				1	EOP Start Time (ns) EOP to EOP Duration (ns)	pArgumen			00:081382		
1,796	1,460	(Order 0)						Execution Start Time (ni)		mufferoffset		1560		
1,797		SetGraphicsRootDescr	iptorTable(0,ptr: 0x0000cff6:					Global 10	country			NULL		
1,798			<pre>iptorTable(0,ptr: 0x0000cff7;</pre>					TOP to SOP Duration (rs) Cache counters		fferOffset		0		
1,799	1,481-1		},3,3,20,0bj#1302,1560,MULL,4					Cache counters General J statist						
1,800	1,481	[MultiDrawInfo]						Geometry counters						
1,001	1,482	[0raw 0]						+ Latency counters 1 printer						
1,002	1,483	[0raw 1]					-1-1	 Memory counters 						
1,803	1,484	[0raw 2]	iptorTable(0.str: 0x0000cff7c					 Pixel counters 						
4,804			iptorTable(0,ptr: 0x0000cffs;					Shader counters Texture counters						
1.005	1,485),1,3,20,0b5#1302,1620,MRAL,4					 Clipping 						
1.807	1,485	[0raw #]												
1,008			iptorTable(0.str: 0x0000cff8r					+ 06						
1,009		SetGraphScsRootDesco	iptorTable(3.str: 0x0000cffs:					+ 605						
1,810	1,486	ExecuteIndirect((),1,1,20,0bj#1302,1640,MULL,4											
4,812		SetGraphScsRootDescr	iptorTable(0,ptr) exceededfie											
1,813		SetGraphScsRootDesci	<pre>iptorTable(3,ptr: 0x0000cffa;</pre>											
-	1,103	A free de la fil e e et i i	1											
4,816		SetGraphScsRootDescr	<pre>iptorTable(0,ptr: 0x0000cffac</pre>											
							•		•					
Pipeline	Outpu	a												
		× •	Global ID 1484 📌 🖒 🕄) 🛅 🖾 🕚	0									
			E Sources	V X // 114	PS running as P	s (click here.	for ins	ruction set documentation)						
UGB Ca	ched Mesh Bu	fler	* Source		der Address) Br der Size: 17544	2.79.70.38.700 butters								
s			P FOE Missing		Rs: 00, SGPRs:	00, User SGPRS	1.26							
luqti			# GPU Instructions	// No	ber of PS inter main // DUMP;									
ader			HWPS running as PS (GID 1-		(CI)									
	itConstantBuf	ferinfo_Linear	Completion	type										
N 2:10	itConstantBul	Serinfo Linear	Compilation Compilar Options		w_b64 s[681	69], exec		// s.ps_state in s0 // aaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaaa						
	1: UGB Caches	d Mesh Butter	E3 Condega Obgoar	1.00	m b64 exec,	exec // arscc		// 00000000001: SITEON/C						
	Chue birow : S	lata			w 532 573, w 532 53, 0			// 0100000000008: 81C90380 // 010000000000C: 82830380						
	gs] : bspindires			1.00	w 532 57, 0			// 000000000010: 05870300						
V Buffler				1.00	w 032 52, 5	32		// 00000000014: 8E82030C						
V Buffler				1.12	w b32 s71, w b32 s70,	55		// 00000000028: 8EC70380 // 0000000000212: 8EC60385						
V Butter direct Ar S					w 0.32 572,			// 000000000000000 BEC8038D						
V Butter drect Ar S ader	d Constant Bull	latinin Linear												
V Buffer direct Ar s ader V 2 : FUR	it:ConstantBuf			C10	ad_dwords8_s[4	8:51], s[6:7],	0x00	// 00000000024: C0900700						
V Buffer direct Ar s ader V 2 : FUR V 3 : glo	balSceneConst	antBuffer		3	ad dwordst site	111], s[2:3],	0x8c	// 000000000034: C0900700 // 000000000035: C084030C // 0000000000035: C094030C				@ 202	1 6	isian Dublishing I
V Buffer direct Ar ader V 2 : FUE V 3 : gloi V Buffer		antBuffer fesh Buffer		0000	ad dwordst site	4:11] 5[2:3] 4:47] 5[72:73 9:23] 5[79:71	0x8c], 0x88], 0x88	// 00000000028: C084030C				© 202	1 Activ	ision Publishing,

Over the previous, eighth generation of hardware we had to adjust our notion of sorting to reflect the underlying hardware realities of the consoles.

For the GCN GPUs this meant understanding the importance of context rolls and how for instance vertex and pixel shader state was not part of a draw context and now shouldn't be a primary part of the sort key.

Although there are values to minimizing those roles--I\$ was a surprising impact to performance, around 250us per frame once we had precaching enabled.

But the larger point here of course is how we reconsider things through the years as hardware and software approaches change.

CLICK

For instance this is how a chunk of our draw loop looks now, which is all indirect execution with multi-draw.

PRIMARY / RING ALLOCATOR, OWNED BY BACKEND THREAD
LINEAR ALLOCATORS, OWNED BY PER FRAME BACKEND DATA / MULTIPLE THREADS
CHUNK 0 CHUNK 1 CHUNK 2 CHUNK 3 CHUNK 4 CHUNK 5 CHUNK 6 CHUNK 7 500
CHUNK 0 CHUNK 2 CHUNK 6
WORKER 1
CHUNK 1 CHUNK 3 CHUNK 4 CHUNK 5 CHUNK 7
© 2021 Activision Publishing, Inc.

I mentioned our approach to memory allocation previously and these tight draw loops touch on this as well. In the core draw loops we may need to peel some memory for constant buffer allocation (and command buffer as well,

but that's another story).

However we don't want to do anything expensive at per-draw granularity.

And we don't want complicated individual allocation schemes, either, such as synchronized multi-threaded heaps. For our approach we have two underlying allocation idioms.

We maintain a primary ring buffer for constant allocation which is fenced to retire chunks and always allow forward progress by the GPU.

However the vast majority of our allocations go through linear allocators that are contained within each of the 'backend data' structures we discussed earlier.

CLICK CLICK

Having that ownership aligned with the 'backend data' allows us to avoid any sort of complicated synchronization with the GPU as we already fence on 'backend data' granularity.

GEOMETRY	TYPE				FRAM	-	S	IMPLE LINEAR	VARIABLE A	LLOCATION
DRAW GROUP	DRAW	DRAW]							
DRAW GROUP	DRAW	DRAW]							
GEOMETRY	TYPE									
DRAW GROUP	DRAW	DRAW]							
DRAW GROUP	DRAW	DRAW]							

In order to avoid expensive per draw costs we reserve chunks from the allocator at a higher level using standard atomics.

CLICK

These chunks are assigned to each of the parallel command buffer generation threads as they need them.

And because that ownership is then per-thread we can simply increment non-atomically within that chunk to each draw as needed.

CLICK

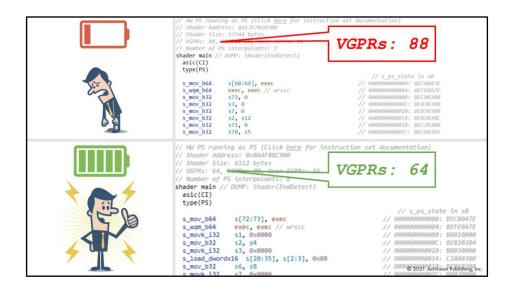
The size of the chunk is based on a calculation of the maximum per-draw reservation multiplied by a limit on the number of surfaces we'll iterate on before breaking the loop.

We can validate this value offline during shader serialization by noting the maximum size of constant buffer references. So we know that per-draw allocations will never fail, but that they may fail at a higher level if we exhaust the linear allocator.

However at that level we can also safely exit our draw loops, which leads to improved resiliency in the renderer.

And if we're super clever, we can use the handoff mechanism we discussed previously to continuing drawing on the backend thread using the ring allocator which can always advance.

One of the downsides of this approach is internal waste/fragmentation if the surface count within the group is low. But that's also indicative of other problems as well in terms of draw grouping efficiency.



I wanted to go back to shader microcode and talk about our approach to building shaders and why it is the way it is.

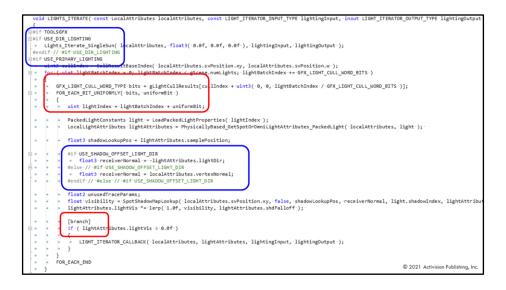
This slide is basically the shared trauma for those of us who spent the past ten years doing microcode optimization.

The GPU architecture in the eighth generation of consoles were sensitive to what we call occupancy.

Which is essentially the number of concurrent workloads that could be processed in-flight at any point in time.

The underlying hardware had a register file which was split between varying and scalar registers depending on whether values were uniform or not across a wavefront.

The shader compilers didn't always make optimizing for this easy, we spent enormous amounts of time analyzing how to improve register usage.



Hitting that magical four-wave occupancy at 64 VGPRs was a holy grail for us for our forward+ opaque shaders. CLICK

To achieve this we spent a significant amount of time hand-building and structuring our lighting loops, decal loops, etc., with bespoke scalarization tricks.

But we also have material composition like any moderately complex rendering system.

CLICK

Many people pursue graph systems but we still rely on essentially textual processing with preprocessor directives to build all of our shaders.

Our tight rein on performance has never made us comfortable with permitting the full level of arbitrary expressiveness in a graph system, even at the cost of iteration.

And because we are shipping games and not engines we can spend the time on this level of specialization and optimization.

Platform: <all></all>	• Config </th <th>All> • Type: <all< th=""><th>> • 1</th><th>Map: mp_apartm</th><th>nents</th><th>• 0.1 P.</th><th></th><th>0000</th><th>0000</th><th>0000</th><th>~~~~</th><th>~~~~</th><th>~~~~</th><th></th><th>~~~~</th></all<></th>	All> • Type: <all< th=""><th>> • 1</th><th>Map: mp_apartm</th><th>nents</th><th>• 0.1 P.</th><th></th><th>0000</th><th>0000</th><th>0000</th><th>~~~~</th><th>~~~~</th><th>~~~~</th><th></th><th>~~~~</th></all<>	> • 1	Map: mp_apartm	nents	• 0.1 P.		0000	0000	0000	~~~~	~~~~	~~~~		~~~~
Build Builder	CL#	Date	Samples	Note			*******		2222	2222	2222				
release RFAZIO-764	1756660	5/1/2014 2:25:31 PM	4000					8888	8888	0000	6660	8888		88888	
release RFAZIO-764		4/30/2014 12:11:58 AM	4000			18	20000000	0000	0000	0000	666	0000	2000	0000	0000
		4/25/2014 9:01:25 AM	4000			- 16		0000	- 19 9 0	and the second second		linici.	8999		
release ERANRICH-7		4/25/2014 12:13:01 AM	4000			- 15		500Q	1.557	The second	R8	25 100	80.0		8000
release ERANRICH-7 release RFAZIO-764		4/24/2014 3:30:38 AM 4/23/2014 3:41:35 AM	4000			- 10		2008	10		20 No.	4	secon -	00000	
release RFAZIO-764		4/16/2014 8:57:22 AM	4000			18		20 ma	14 1	19-19-20	1 8	1000	Here's	80000	
release RFAZIO-764		4/14/2014 7:02:09 PM	4000			- IE		10.1	e cardo de	(m)	1.000	- 1 · · ·	100	0000	
release RFAZIO-764		4/9/2014 7:04:04 PM	4000			- HK	20000000	12.2		T.I.	444		Boa	0000	
release KMYERS-764		4/9/2014 5:19:46 PM	14888			- 16		156-6		1 Second	1.4	and a la	22	0000	
release RFAZIO-764	1749457	4/8/2014 6:32:18 PM	4000			- 15		100	10 10	1.1.4	<u></u>		4		
release RFAZIO-764	1747601	4/2/2014 8:57:59 PM	4000			- 10	0000000	56668	10422-84	1.00	1160	1	·		
						18		2022	3333			888			888
							Counter CPU GPU	Linkte							
	Averages	1					Counters CPU GPU			Ta	Date a	Secure	Same	See as	South 10
Samples	-	-				F	Position	FPS	Prime	Tris	Static m			Spot pri	Spot the
	100	2				F	Position (-200750.1542) 90		291	598156	296	Sun prime 0	Sun tris	Spot pri 0	Spot tra
FPS	100	4				F ((Position	FPS 87.3				0	0	0	0
FPS	100 80.4 190	0 4 5				F () E ()	Position -200750,1542) 90 -150,-800,1541) 270 -50750,1540) 180 (50,-750,1540) 180	FPS 87.3 86.4	291 103	598156 111276 119906 181595	296 47	0 0 0	0 0 0	0	0 0 0
	100	0 4 5				F ()	Position -200,-750,1542) 90 -150,-800,1541) 270 -50,-750,1540) 180 (50,-750,1540) 180 100,-850,1540) 180	FPS 87.3 86.4 85.0 81.8 82.5	291 103 110 164 123	598156 111276 119906 181595 116596	296 47 67 119 83	0 0 0 0	0 0 0 0	0	0 0 0 0
FPS Prims Tris	100 80.4 190	0 4 5 3				E () () ()	Postion (200750,1542) 90 (-150.980,1541) 270 (-50750,1540) 180 (50750,1540) 180 (100850,1540) 180 (-50900,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9	291 103 110 164 123 320	598156 111276 119906 181595 116596 693018	296 47 67 119 83 321		000000000000000000000000000000000000000	0	0000000
FPS Prims Tris Static models	1000 80.4 199 314711 144	5				H () () () () () () () () () () () () ()	Position 200750,1542) 90 150800,1541) 270 50750,1540) 180 100850,1540) 180 100850,1540) 180 .50900,1540) 90 .200900,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4	291 103 110 164 123 320 271	598156 111276 119906 181595 116596 693018 595905	296 47 67 119 83 321 282		0 0 0 0 0 0 0	0	0 0 0 0
FPS Prims Tris Static models Sun prims	1000 80 31471: 144	0 4 5 5 5				F () () () () () () () () () () () () ()	Postion 200-750,1542) 90 150,900,1541) 270 50,750,1540) 180 (50,750,1540) 180 100,950,1540) 180 (50,900,1540) 90 150,900,1540) 90 150,900,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4 78.6	291 103 110 164 123 320 271 289	598156 111276 119906 181595 116596 693018 595905 513908	296 47 67 119 83 321 282 246	000000000000000000000000000000000000000	0 0 0 0 0 0 0 0	0	0 0 0 0 0 0 0
FPS Prims Tris Static models Sun prims	1000 80.4 199 314711 144	0 4 5 5 5				H () () () () () () () () () (Outlion (200,750,1542) 90 -150,800,1541) 270 -50,750,1540) 180 150,750,1540) 180 100,850,1540) 180 -50,000,1540) 90 -200,900,1540) 90 -50,000,1540) 90 -50,000,1540) 90 -50,000,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4 78.6 82.8	291 103 110 164 123 320 271 289 276	598156 111276 119906 181595 116596 693018 595905 513908 510011	296 47 67 119 83 321 282 246 231	000000000000000000000000000000000000000	0 0 0 0 0 0 0 0 0 0	0	000000000000000000000000000000000000000
FPS Prims Tris Static models Sun prims Sun tris	1000 80 31471: 144					E () () () () () () () () () () () () ()	Postion 200-750,1542) 90 150,900,1541) 270 50,750,1540) 180 (50,750,1540) 180 100,950,1540) 180 (50,900,1540) 90 150,900,1540) 90 150,900,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4 78.6	291 103 110 164 123 320 271 289	598156 111276 119906 181595 116596 693018 595905 513908	296 47 67 119 83 321 282 246	000000000000000000000000000000000000000	0 0 0 0 0 0 0 0	0	0 0 0 0 0 0 0
FPS Prims Tris Static models Sun prims Sun tris Spot prims	1000 80.4 314712 144					E	Ostion (200,750,1542) 90 150,800,1541) 270 (50,750,1540) 180 (50,750,1540) 180 (50,750,1540) 180 (50,300,1540) 90 (50,900,1540) 90 (50,900,1540) 90 (50,900,1540) 90 (50,900,1540) 90 (50,900,1540) 90	FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4 78.6 82.8 73.0	291 103 110 164 123 320 271 289 276 304	598156 111276 119906 181595 116596 693018 595905 513908 510011 702711	296 47 67 119 83 321 282 246 231 301	000000000000000000000000000000000000000		0	000000000000000000000000000000000000000
FPS Prims Tris Static models Sun prims Sun tris	1000 80.4 314712 144					E	Costion (200,750,1542) 90 (150,800,1541) 270 (50,750,1540) 180 (50,750,1540) 180 (100,850,1540) 180 (100,850,1540) 180 (150,900,1540) 90 (150,900,1540) 90 (250,950,1540) 90 (100,950,1540) 90 (FPS 87.3 86.4 85.0 81.8 82.5 78.9 82.4 78.6 82.8 73.0 78.8	291 103 110 164 123 320 271 289 276 304 253	598156 111276 119906 181595 116596 693018 595905 513908 510011 702711 416740	296 47 67 119 83 321 282 246 231 301 200			0	

But any permutation system becomes susceptible to combinatoric explosion at some point.

On a previous title for instance we peaked at around 400k unique pixel shaders for a given map.

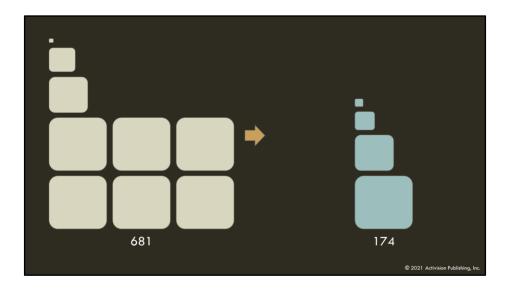
Tracking performance across all of those is impossible, and we pursued several approaches.

Automation. Telemetry system where shader statistics are recorded per CI run and then diffs are produced, can examine these from data builds, but also for local changes you can queue preflight checks and view reports

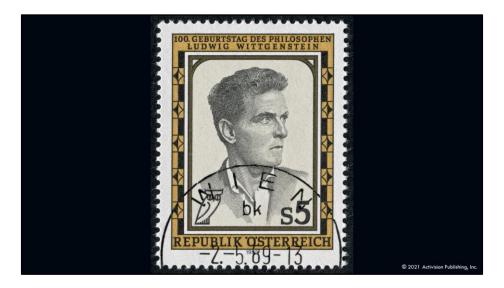
This is our tool, fpsTool, that takes thousands of samples in real game levels and builds aggregate performance data.

In what is a massive defeat for our industry's dignity and self-respect, we also brought back infrastructure to explore shader compiler tunable space via exhaustive compilation and custom performance evaluation heuristics.

Some of you will have gone through this with nvshaderperf back in the PS3 era and random scheduler seed permutation.



We also aggressively pursued static reductions of our techniques which subsequently led to reductions in permutations. We did this via careful folding of functionality into static/scalar branches in our base forward+ shaders. On some olders titles we had 681 separate techniques, and on more recent ones we got that down to 174.



Our final philosophical interlude.

In the seventh generation of consoles, the Xbox 360 and the PS3, I felt like, as a systems and rendering programmer, our primary work was to learn to maximize CPU parallelism.

And this especially manifested, at least for us, in parallel command buffer generation.

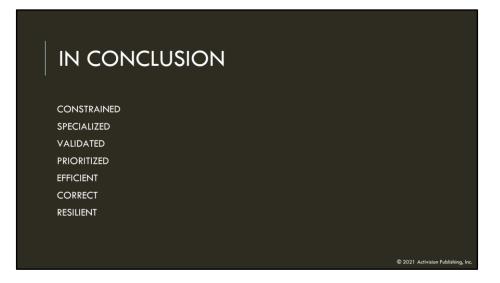
In the eighth generation of consoles, it felt like our primary work was in understanding how to generate efficient GPU microcode to exploit GPU parallelism.

We spent a lot of time working with shader compilers and learning how to coax them to generate optimal microcode. What will be our primary legacy for the ninth generation of consoles?

Will it be a solution to the practical problems of raytracing? That feels sort of obvious and banal.

Maybe something else entirely? I'm curious as to what you think.

[WITTGENSTEIN]



To wrap up. When I think of the benefits we try to exploit because of our global knowledge of the use of our technology, they are that our systems are:

CONSTRAINED.

Many optimization opportunities come from the careful choice of constraints and thus can be SPECIALIZED.

Much of our lighting can be pre-calculated offline because our world is largely static.

Our game logic can operate at a decoupled rate because we always operate in a client-server model.

Choosing the right constraints is one of the most important things for you to do architecturally.

VALIDATED.

As workloads and underlying platforms change you need to revisit your choices and validate they're still the right ones. Good historical record-keeping helps inform that validation and the work you're doing today. PRIORITIZED.

Prioritize your work by its exposure to the user, and make the effort to napkin math the impact.

EFFICIENT.

Think of optimization as a minimization exercise for energy through a system. CORRECT.

Engineer systems for simplicity of ownership of memory to better reason about synchronization. RESILIENT.

Encode your assumptions at the appropriate level of operation and do the work to ensure robustness.



Thanks everyone, I hope you enjoyed the talk.

Also some call outs here from folks at Activision as well as at Sony and Microsoft who helped permit this presentation to be as good as I wanted it to be.

REFERENCES

[HARGREAVES19] D3D12 Background Processing - Spec, Shawn Hargreaves, 2019

2021 Activision Publishing, Inc